ABSTRACT

Disclosed is a method an apparatus for selectively providing additional inputs into a logic block, such as a LAB of a PLD, carrying out a logic function requiring a relatively high number of inputs. A PLD in accordance with the present invention includes at least first and second LABs. A plurality of signal lines are capable of driving the second LAB and a plurality of output lines are driven by the first LAB. The PLD also includes a swap multiplexer (MUX) having a first selectable input capable of being driven by the output lines and a second selectable input capable of being driven by the signal lines. An output of the swap MUX is capable of driving the first LAB.